

D8KB1 THRU D8KB10 D3K

1.Input pin(+) 2.Input pin(~)

D3K

3.Output Anode(~)

4.Output Cathode(-)

1

2 3

8.0A GLASS PASSIVATED BRIDGE RECTIFIER Reverse Voltage - 100 to 1000 V Forward Current – 8.0A

FEATURES

High current capability
Low forward voltage drop
Glass Passivated Chip Junction

♦Low power loss, high efficiency

Lead free in comply with EU RoHS 2011/65/EU directives

MECHANICAL DATA

Case: D3K
Terminals: Solderable per MIL-STD-202E, Method 208C
Case:UL-94 Class V-0 recognized Flane Retardant Epoxy

Maximum Ratings and Electrical characteristics

Ratings at 25 °C ambient temperature unless otherwise specified.

Parameter	Symbols	D8KB1	D8KB2	D8KB4	D8KB6	D8KB8	D8KB10	Units
Maximum Repetitive Peak Reverse Voltage	V _{RRM}	100	200	400	600	800	1000	v
Maximum RMS voltage	V _{RMS}	70	140	280	420	560	700	v
Maximum DC Blocking Voltage	V _{DC}	100	200	400	600	800	1000	v
Average Rectified Output Current	I _(AV)	8					Α	
Peak Forward Surge Current 8.3 ms Single Half Sine-Wave Superimposed on Rated Load (JEDEC method)	I _{FSM}	170					Α	
Forward Voltage per element @lF =6.0A DC	V _F	1.1				v		
Maximum DC Reverse Current at @Ta=25°C Rated DC Blocking Voltage @Ta=125°C	I _R	5.0 500					μΑ	
I2t Rating for Fusing(3ms≤t≤8.3ms)	l ² t	120				A ² S		
Typical junction capacitance (Note 1)	CJ	56				pF		
Maximum Typical Thermal resistance	R _{0JA}	55				°C/W		
Operating and Storage Temperature Range	Tj, T _{stg}	-55 ~ +150			°C			

(1) Measured at 1 MHz and applied reverse voltage of 4 V D.C



Ratings and Characteristic Curves

FIG. 1- DERATING CURVE OUTPUT RECTIFIED CURRENT

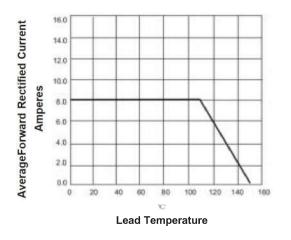
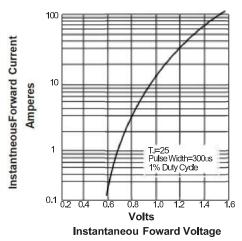
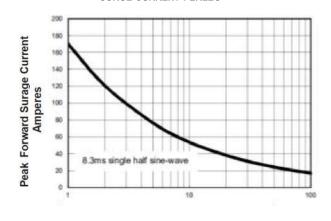


FIG. 3-TYPICAL FORWARD VOLTAGE CHARACTERISTICS

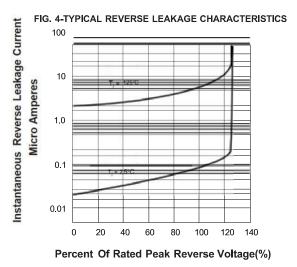


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FIG. 2-MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT PERLEG

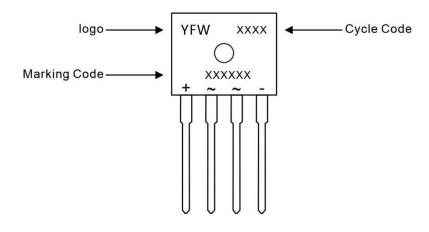


Number of cycles





Marking Diagram

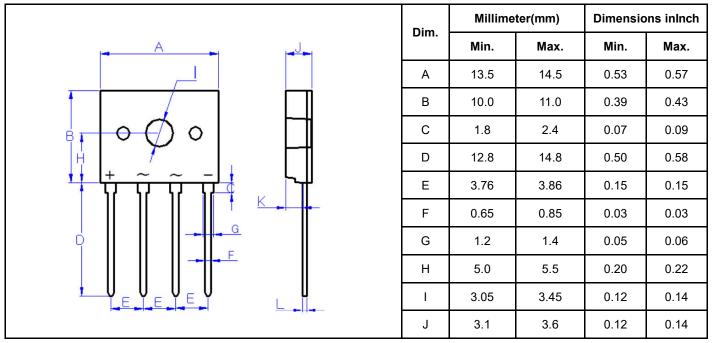


Ordering information

Package	Packing Description	Packing Quantity		
D3K	bulk	500PCS/Box 5000PCS/Carton		

Package Dimensions

D3K





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