

ESD Protector

Features

- 100W peak pulse power per line ($t_P = 8/20\mu s$)
- DFN1006 package
- Replacement for MLV(0402)
- Bidirectional configurations
- Response time is typically < 1ns
- Low clamping voltage
- RoHS compliant
- Transient protection for data lines to
 IEC61000-4-2(ESD) ±15KV(air), ±8KV(contact);
 IEC61000-4-4 (EFT) 40A (5/50ns)

Mechanical characteristics

- Lead finish:100% matte Sn(Tin)
- Mounting position: Any
- Qualified max reflow temperature:260 ℃
- Device meets MSL 1 requirements
- Pure tin plating: 7 ~ 17 um
- Pin flatness:≤3mil

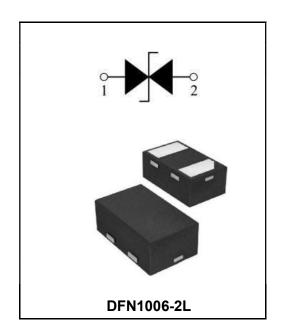
Description

The ESD9BZ5V0C protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, low operating voltage. It gives designer the flexibility to protect one bi-directional line in applications where arrays are not practical.

Rev: BDK

Application

- Cellular phones
- Portable devices
- Digital cameras
- Power supplies

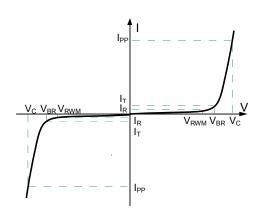


Marking Code		
ESD9BZ5V0C	С	



Electronics Parameter

Symbol	Parameter	
V _{RWM}	Peak Reverse Working Voltage	
I _R	Reverse Leakage Current @ V _{RWM}	
V_{BR}	Breakdown Voltage @ I⊤	
I _T	Test Current	
I _{PP}	Maximum Reverse Peak Pulse Current	
Vc	Clamping Voltage @ IPP	
P _{PP}	Peak Pulse Power	
CJ	Junction Capacitance	
I _F	Forward Current	
V _F	Forward Voltage @ I _F	



Electrical Characteristics(TA = 25 °C unless otherwise specified)

Rev: BDK

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Peak Reverse Working Voltage	V_{RWM}				5	V
Breakdown Voltage	V_{BR}	I _t = 1mA	5.6		8.5	V
Reverse Leakage Current	I _R	V _{RWM} = 5V T=25℃			1.0	μA
Maximum Reverse Peak Pulse Current	I _{PP}			5.5		Α
Clamping Voltage	V _C	I _{PP} =1A			10	V
Clamping Voltage	V _C	I _{PP} =3A			15	V
Clamping Voltage	Vc	I _{PP} =5A			21	V
Junction Capacitance	C _j	V _R =0V f = 1MHz		0.3		pF

Absolute maximum rating

Rating	Symbol	Value	Units
Peak Pulse Power (t _p =8/20µs)	P _{pp}	100	W
Operating Temperature	TJ	-55 to +150	$^{\circ}\! C$
Storage Temperature	T _{STG}	-55 to +150	$^{\circ}$



Typical Characteristics

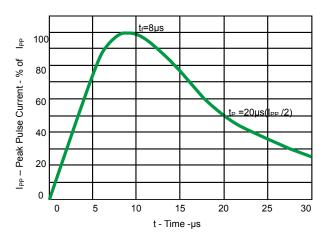


Fig 1.Pulse Waveform

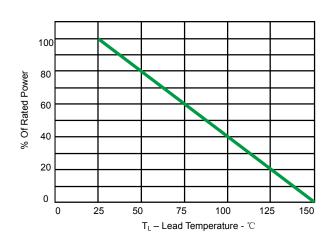


Fig 2.Power Derating Curve

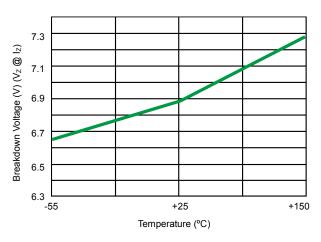


Fig 3.Typical Breakdown Voltage vs. Temperature

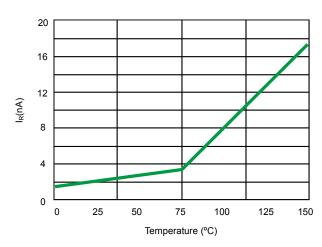


Fig 4.Typical Leakage Current vs. Temperature

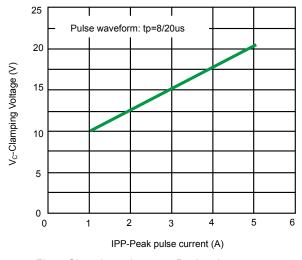


Fig 5. Clamping voltage vs. Peak pulse current

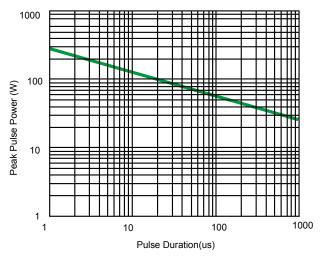
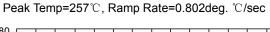
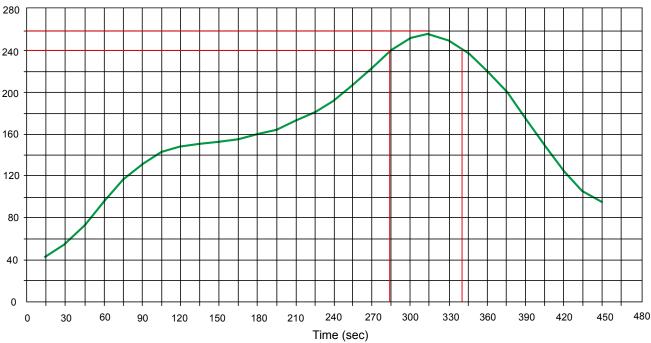


Fig 6. Non-Repetitive Peak Pulse Power vs. Pulse time



Solder Reflow Recommendation





Solder Reflow Recommendation

For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

- Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- Do not make false economies and save copper for the ground connection.

- Place via holes to ground as close as possible to the anode of the TVS diode.
- Use as many via holes as possible for the ground connection.
- Keep the length of via holes in mind! The longer the more inductance they will have.

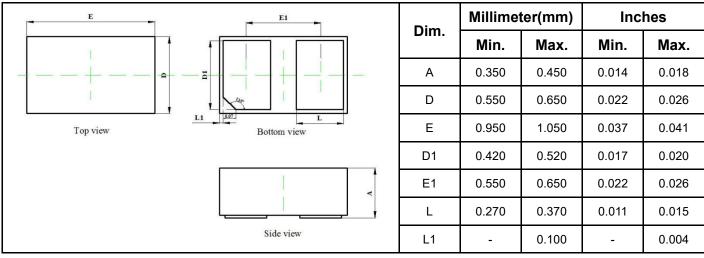


Ordering information

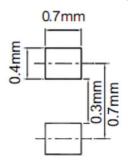
Package	Packing Description	Packing Quantity
DFN1006-2L	Tape/Reel,7"reel	10000PCS/Reel 400000PCS/Carton

Package Dimensions

DFN1006-2L



The recommended mounting pad size





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